

This application is a continuation of co-pending U.S. Serial No. 09/470,973 filed on December 22, 1999 which is a continuation-in-part of U.S. Serial No. 09/035,175 filed on March 4, 1998 which is a continuation-in-part of U. S. Serial Nos. 08/841,409, 08/837,702 and 08/837,714 all filed on April 22, 1997. Further, the following U. S. patent applications filed March 4, 1998, Serial No. 09/034,687, entitled "Digital Isolation System With Data Scrambling" by George Tyson Tuttle et al; Serial No. 09/034,456, entitled "Digital Isolation With ADC Offset Calibration; by Andrew W. Krone et al.; Serial No. 09/034,455, entitled "Ring-Detect Interface Circuitry and Method for a Communication System" by Timothy J. Dupuis et al.; Serial No. 09/035,779, entitled "Call Progress Monitor Circuitry and Method for a Communication System" by Timothy J. Dupuis et al.; Serial No. 09/034,683, entitled "External Resistor and Method to Minimize Power Dissipation in DC Holding Circuitry for a Communication System" by Jeffrey W. Scott et al.; Serial No. 09/034,620, entitled "Caller ID Circuit Powered Through Hookswitch Devices" by Jeffrey W. Scott et al.; and Serial No. 09/034,682, entitled "Framed Delta Sigma Data With Unlikely Delta Sigma Data Patterns" by Andrew W. Krone et al., are expressly incorporated herein by reference.

On page 38, replace the Abstract from lines 3-17 with:

An isolation system for terminating a phone line is provided. The invention may comprise a capacitive isolation barrier across which a digital signal is communicated. Clock recovery circuitry may be employed on one side of the isolation barrier to extract timing information from the digital signal communicated across the barrier. An isolated power supply may also be provided on the isolated side of the barrier, whereby direct current is generated in response to the digital data received across the isolation barrier. A bidirectional isolation system is provided whereby bidirectional communication of digital signals is accomplished using a single pair of isolation capacitors. In preferred embodiments, the digital data communicated across the barrier consists of digital delta-sigma data signals multiplexed in time with other digital control, signaling and framing information. Finally, the isolation system may include a pulse transformer to